MOD_PSE84_SOM PSOC™ Edge E84 SoM

CONTENTS

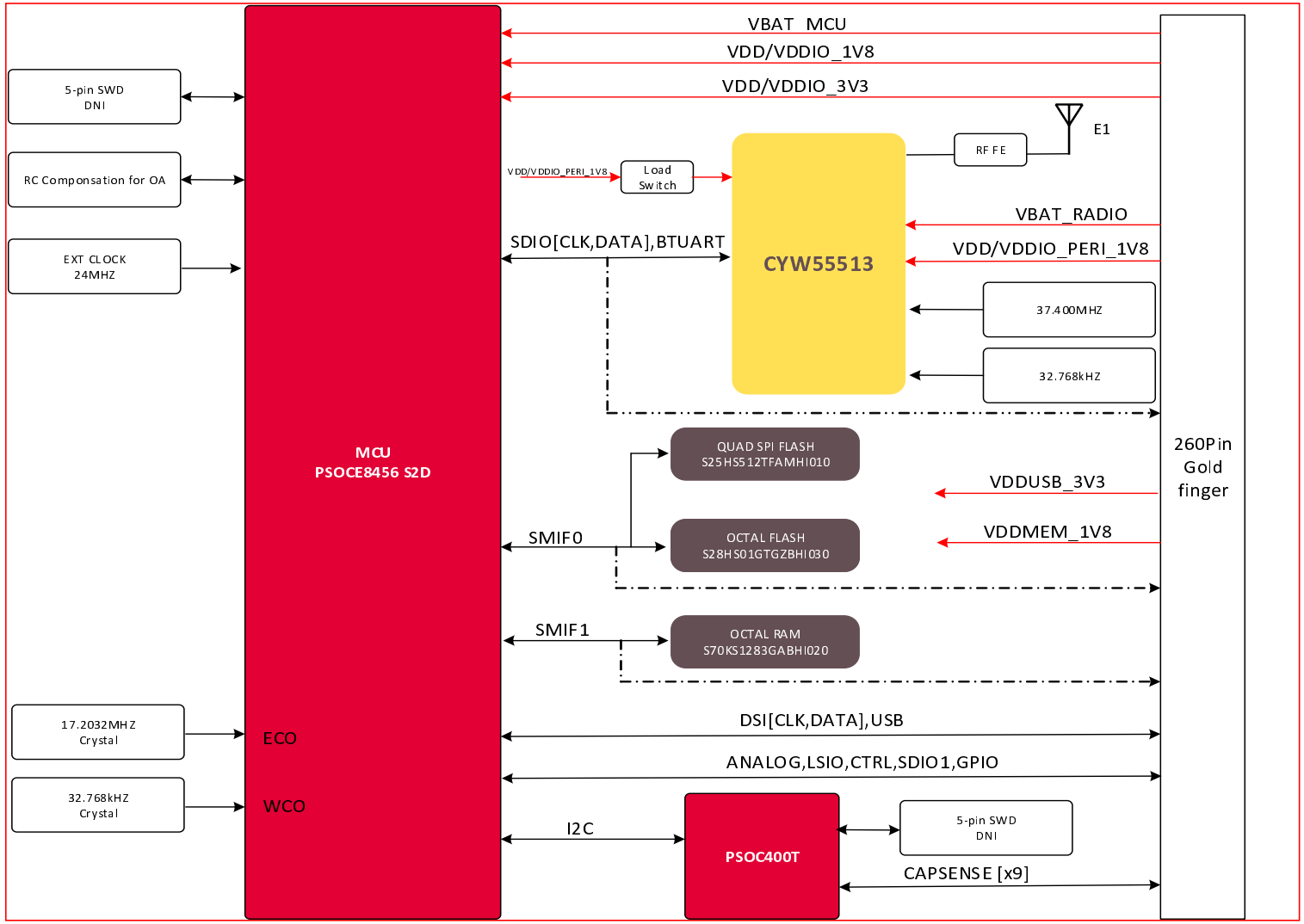
PAGE	DESCRIPTION
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Drawing Numbers

PCBA	121-60673-01
PCB	600-60673-01
FAB DRW	610-60673-01
ASSY DRW	620-60673-01
SCH DRW	630-60673-01

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SCH Title : PSOC™ Edge E84 SoM				
Page Title: Title Page				
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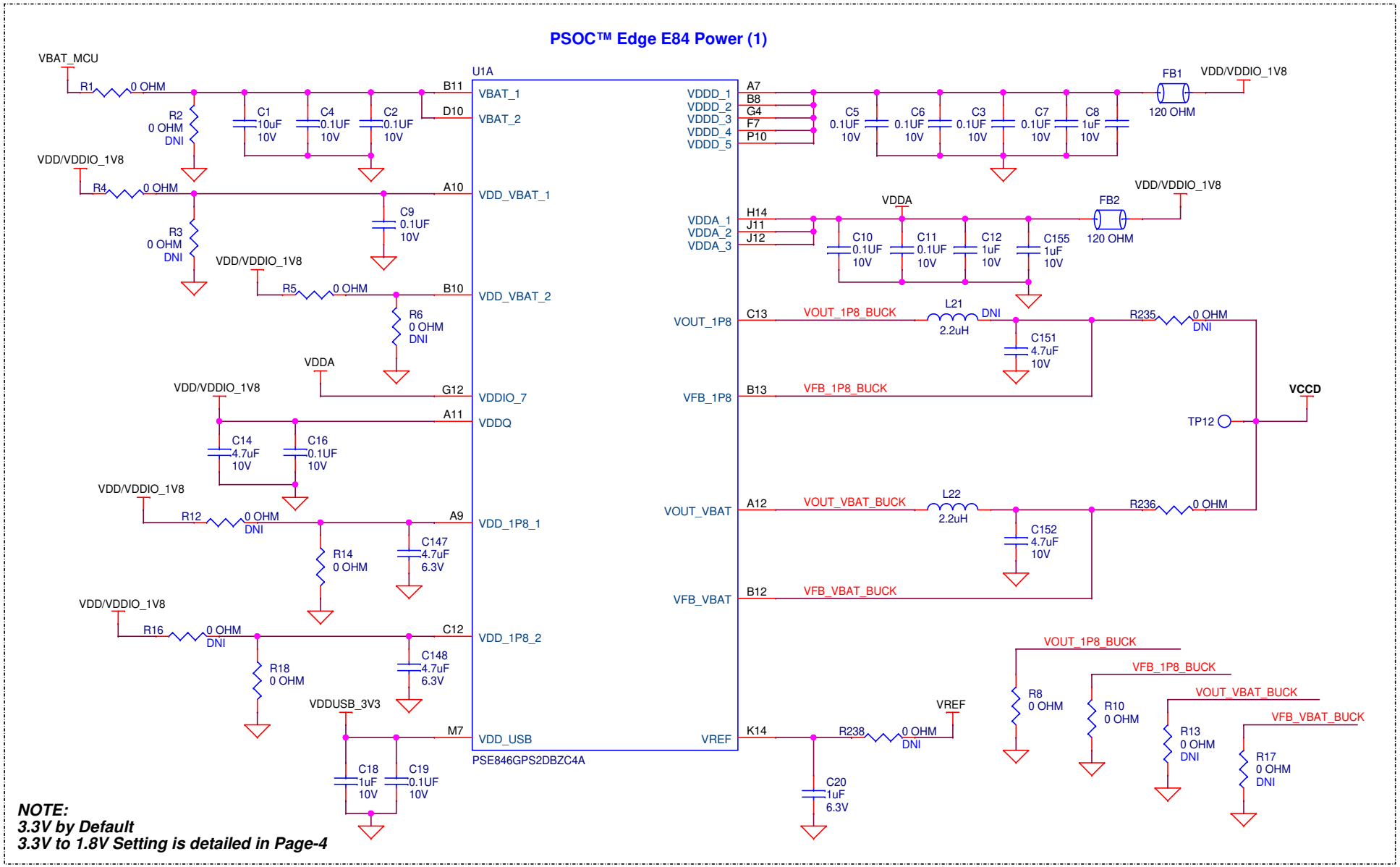
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SCH Title : PSOC™ Edge E84 SoM

Page Title: Block Diagram

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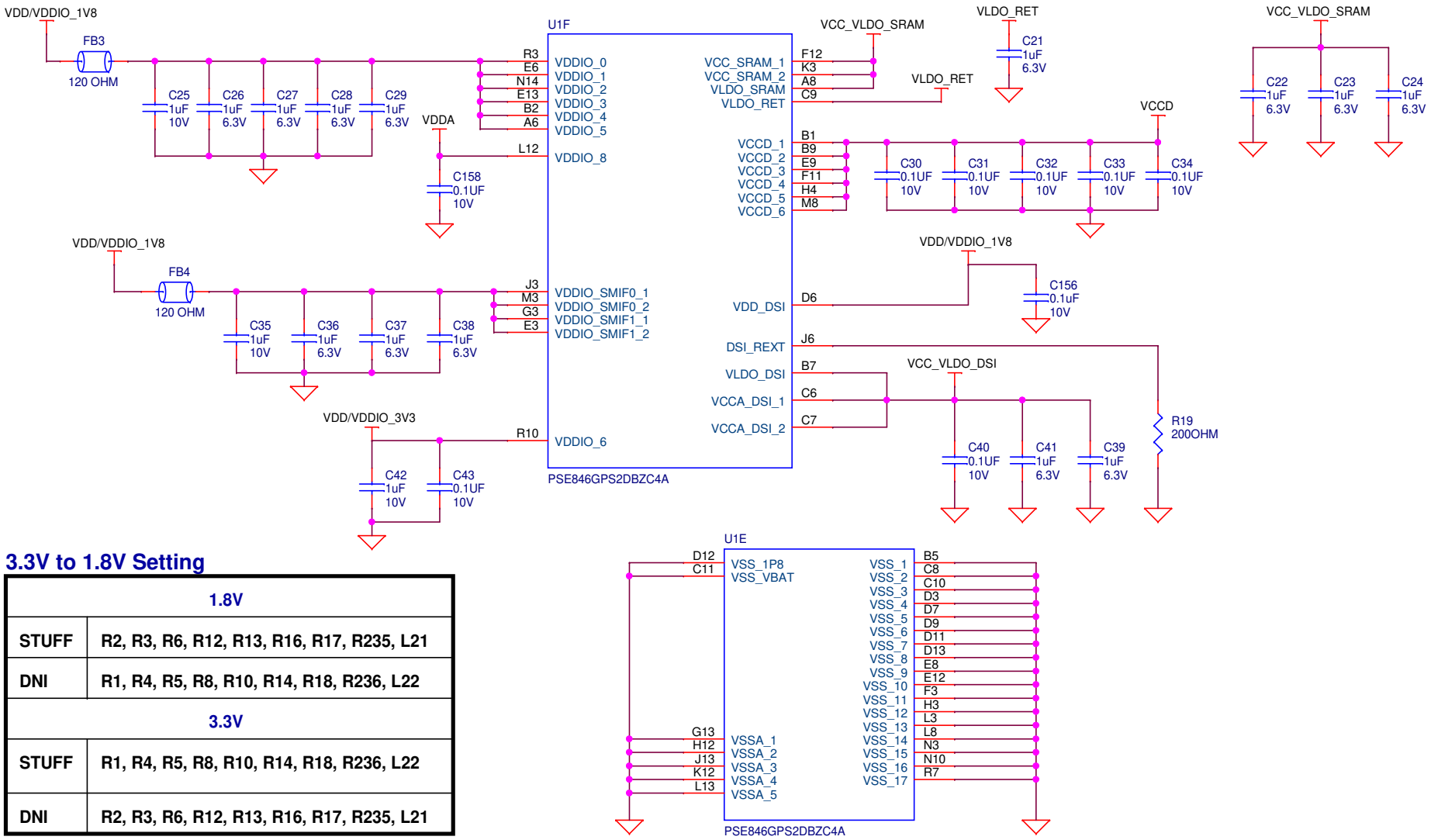
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SCH Title : PSOC™ Edge E84 SoM				
Page Title: MCU Power Selection_1				
Size	Document Number	Drawn By	Approved By	Rev
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PSOC™ Edge E84 Power (2)

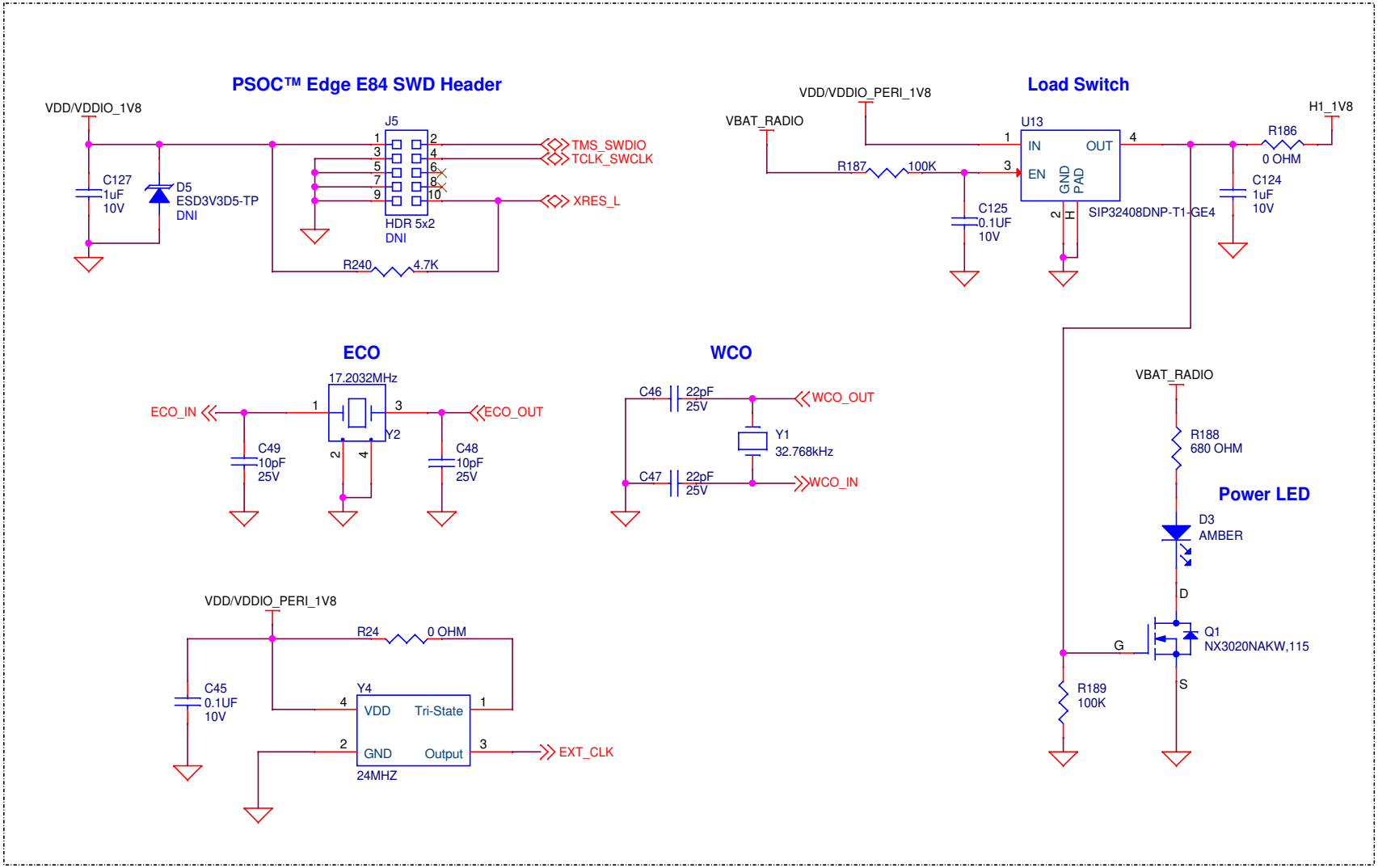


3.3V to 1.8V Setting

1.8V	
STUFF	R2, R3, R6, R12, R13, R16, R17, R235, L21
DNI	R1, R4, R5, R8, R10, R14, R18, R236, L22
3.3V	
STUFF	R1, R4, R5, R8, R10, R14, R18, R236, L22
DNI	R2, R3, R6, R12, R13, R16, R17, R235, L21

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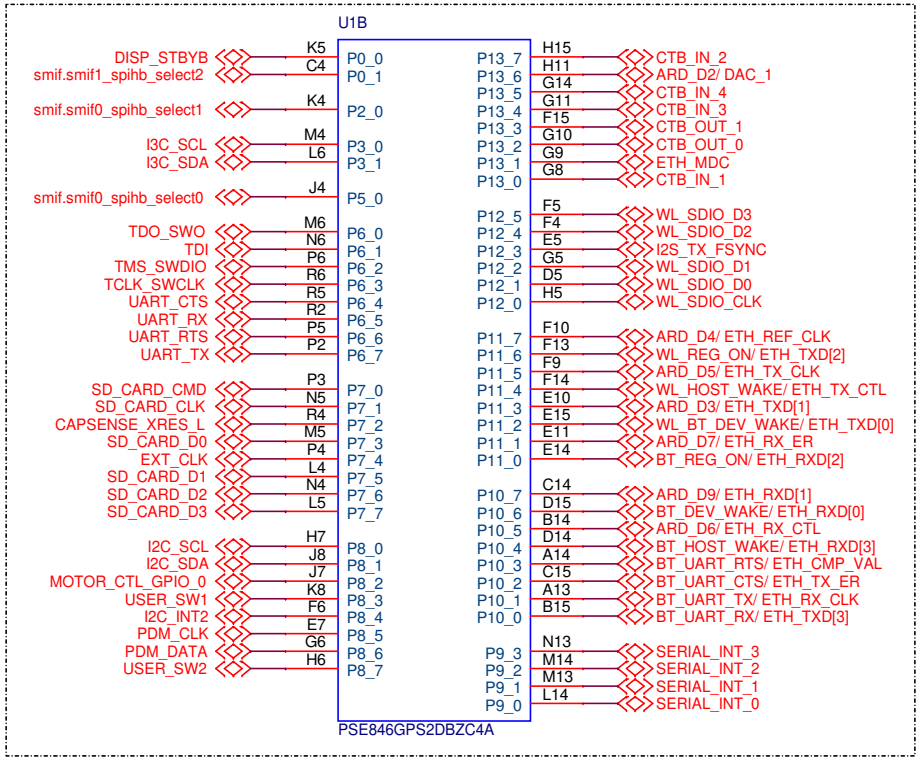
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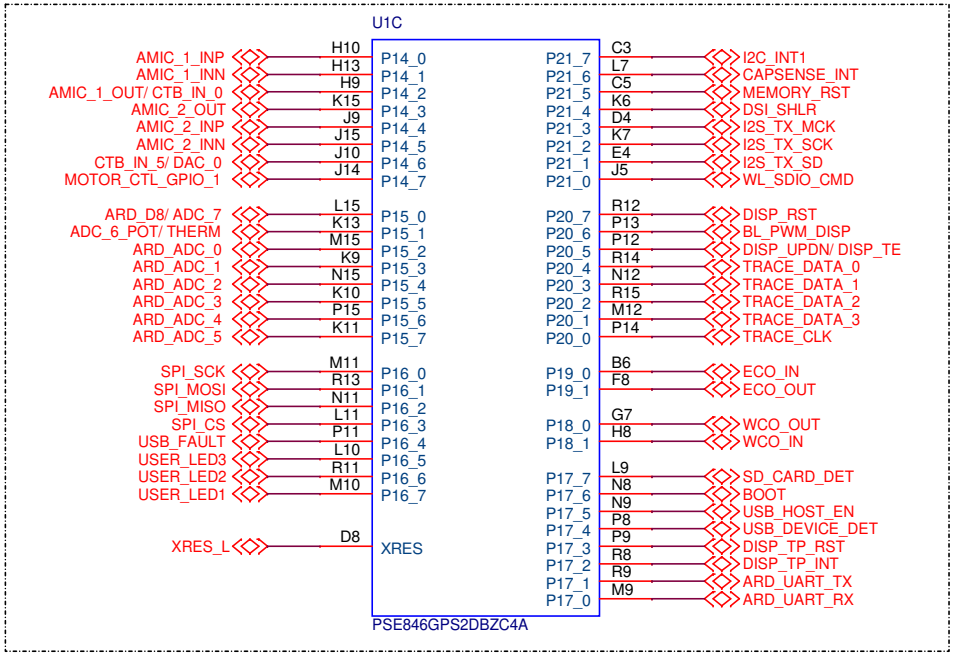


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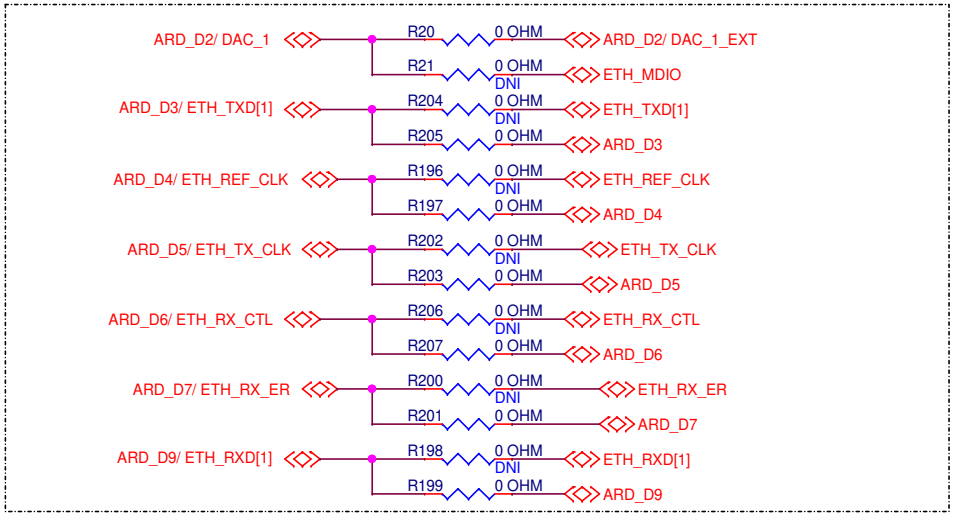
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Page Title: MCU Miscellaneous				
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A4	**630-60673-01**	**RAJB**	**RKPM**	**4.1**
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PSOC™ Edge E84 GPIO mapping (1)





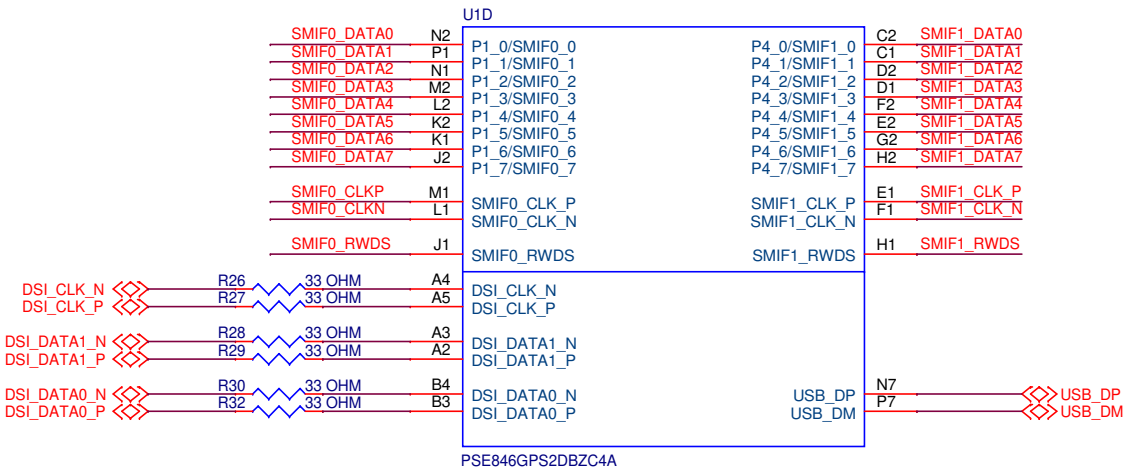


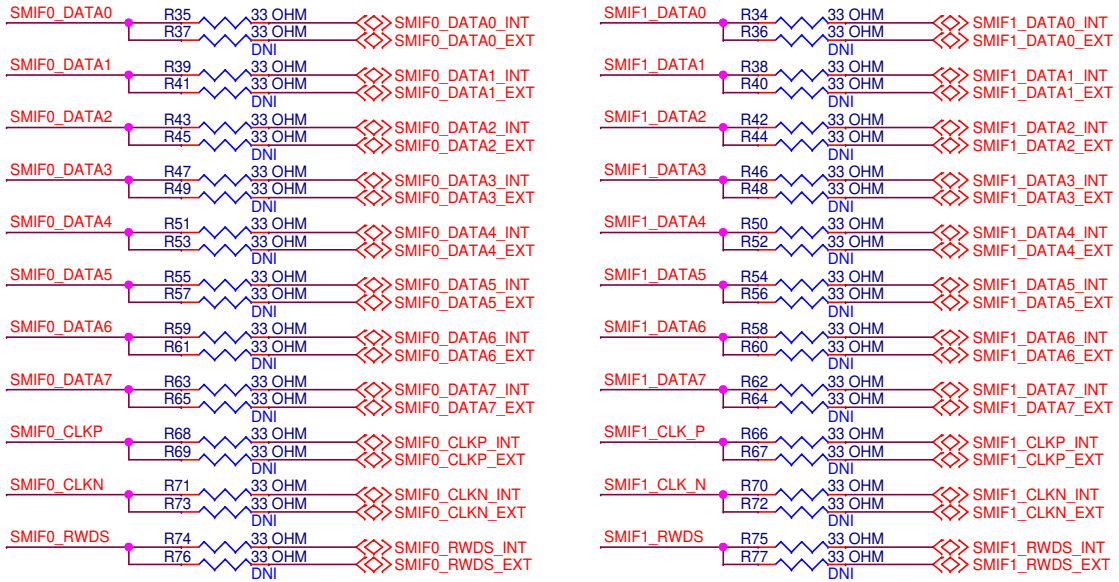


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SCH Title : PSOC™ Edge E84 SoM				
Page Title: MCU Signals_1				
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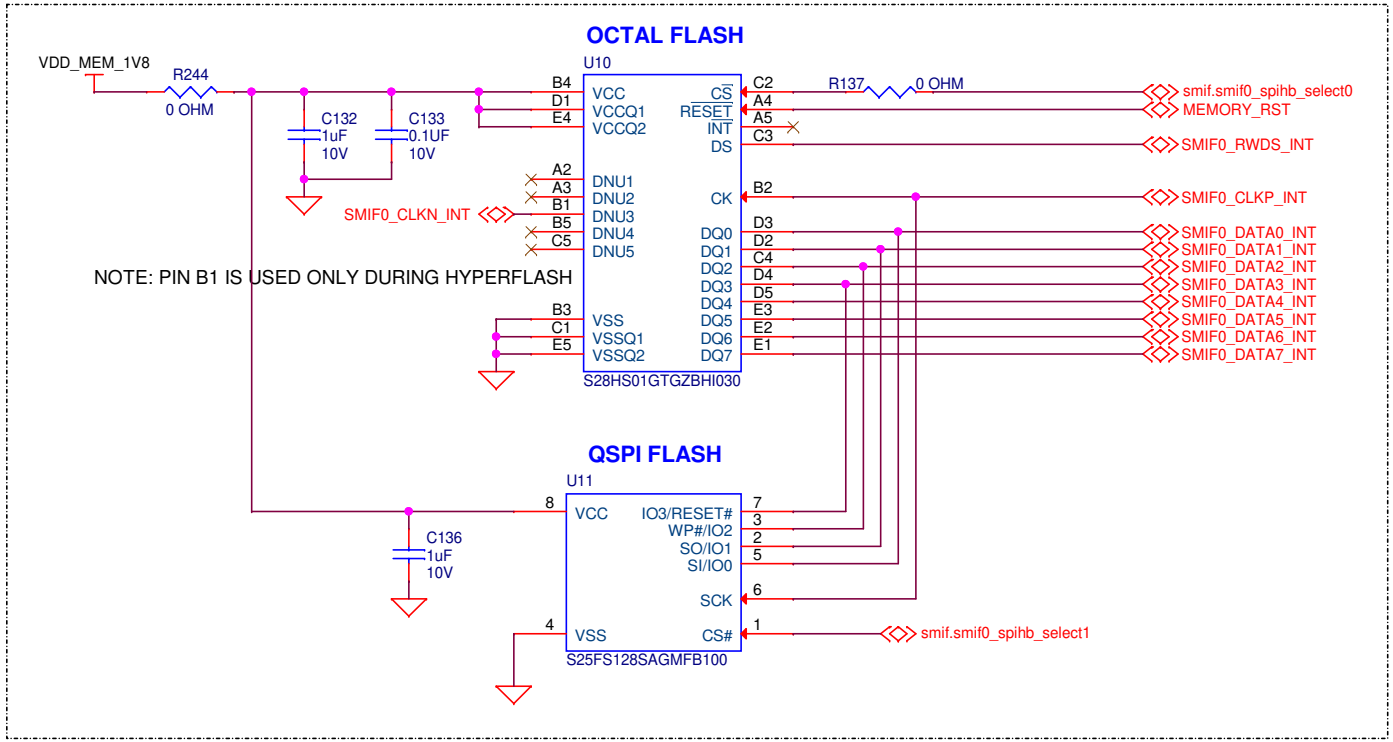
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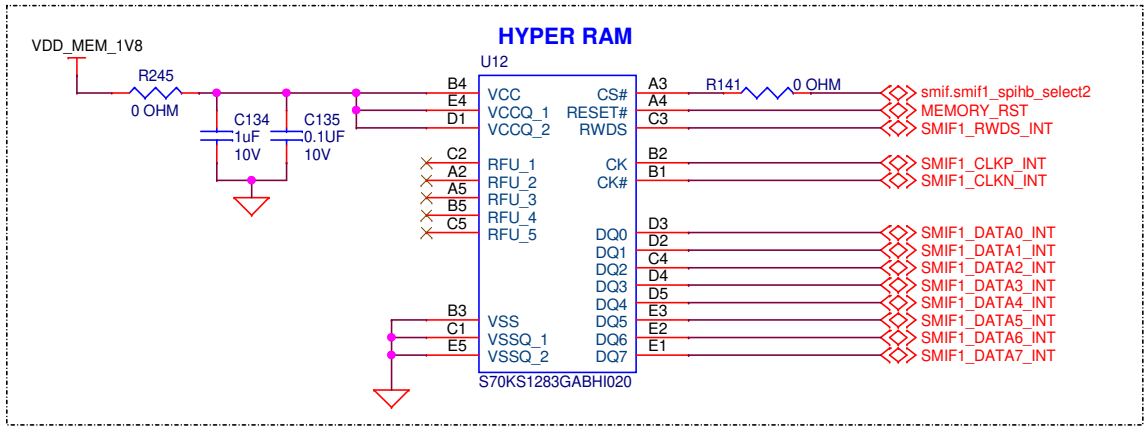


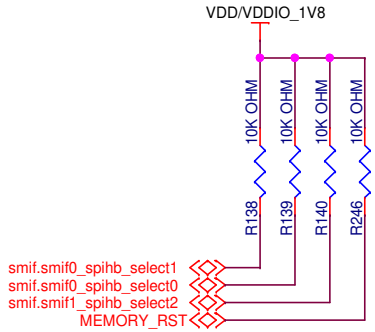


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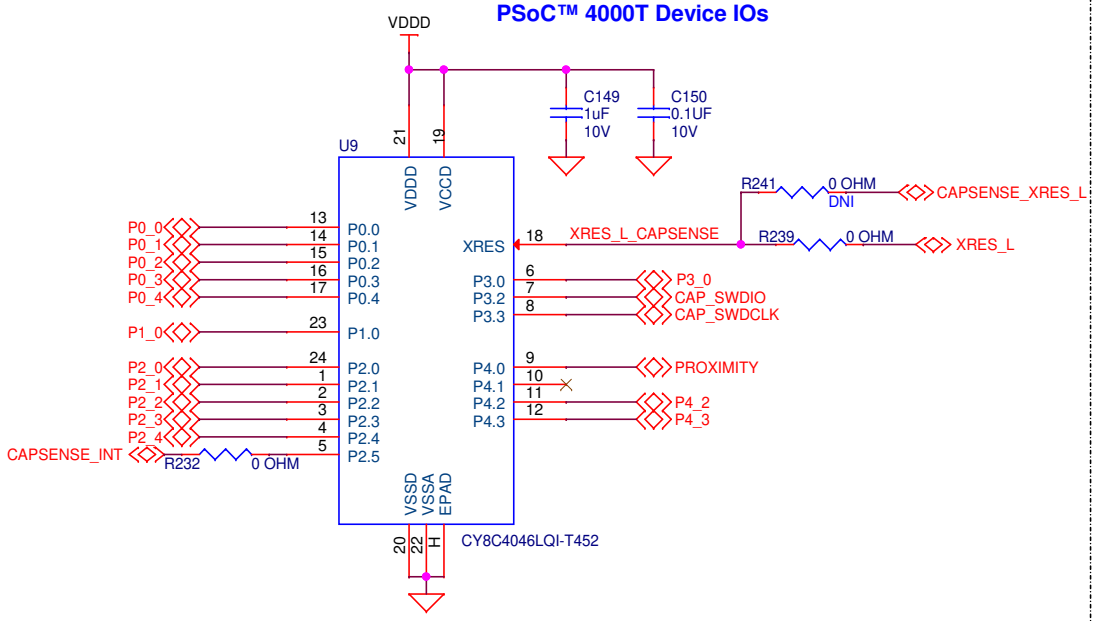




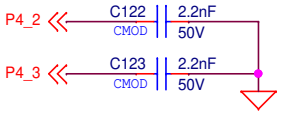
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SCH Title : **PSOC™ Edge E84 SoM**				
Page Title: **SMIF Interface**				
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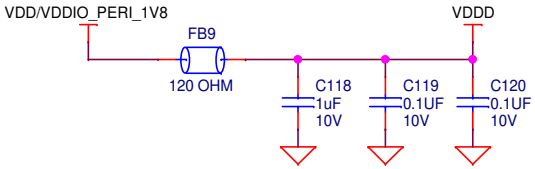
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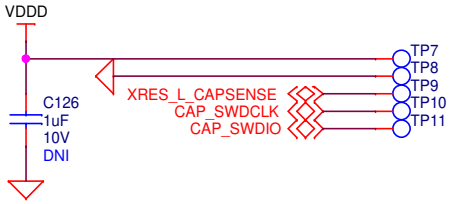
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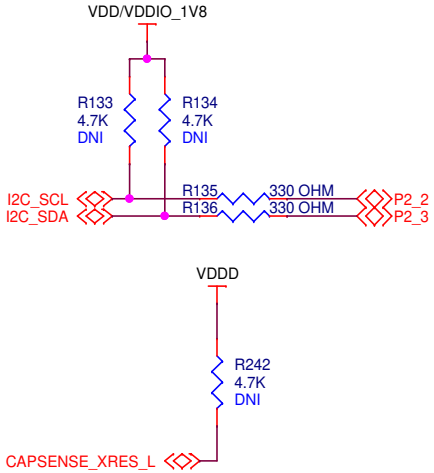
PSoC™ 4000T Device Power



PSoC™ 4000T Device 5-pin SWD Test Point



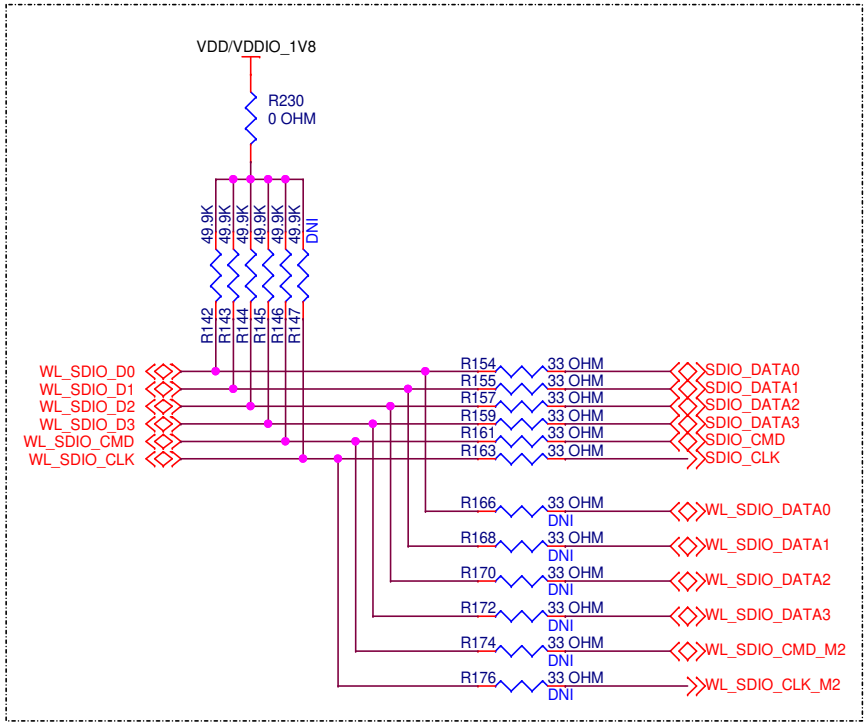
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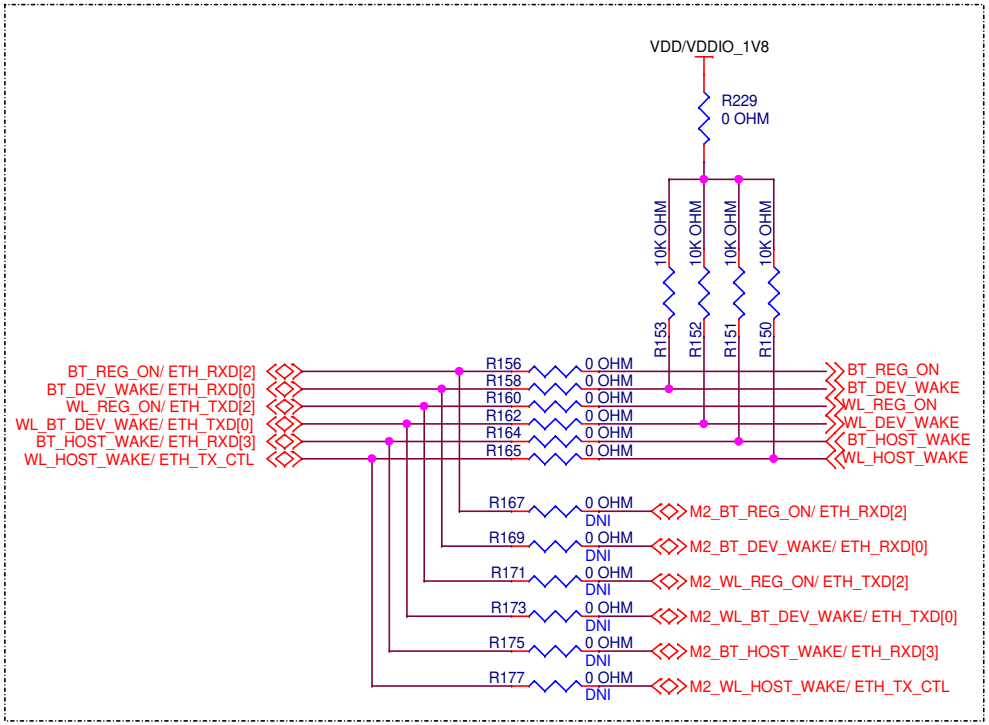


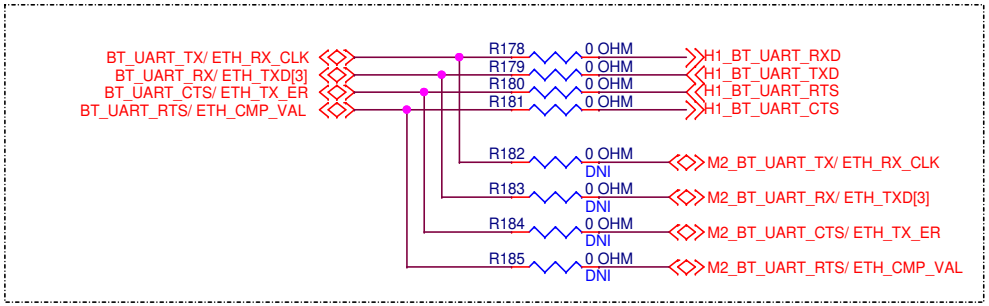
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WiFi/BT multiplexing



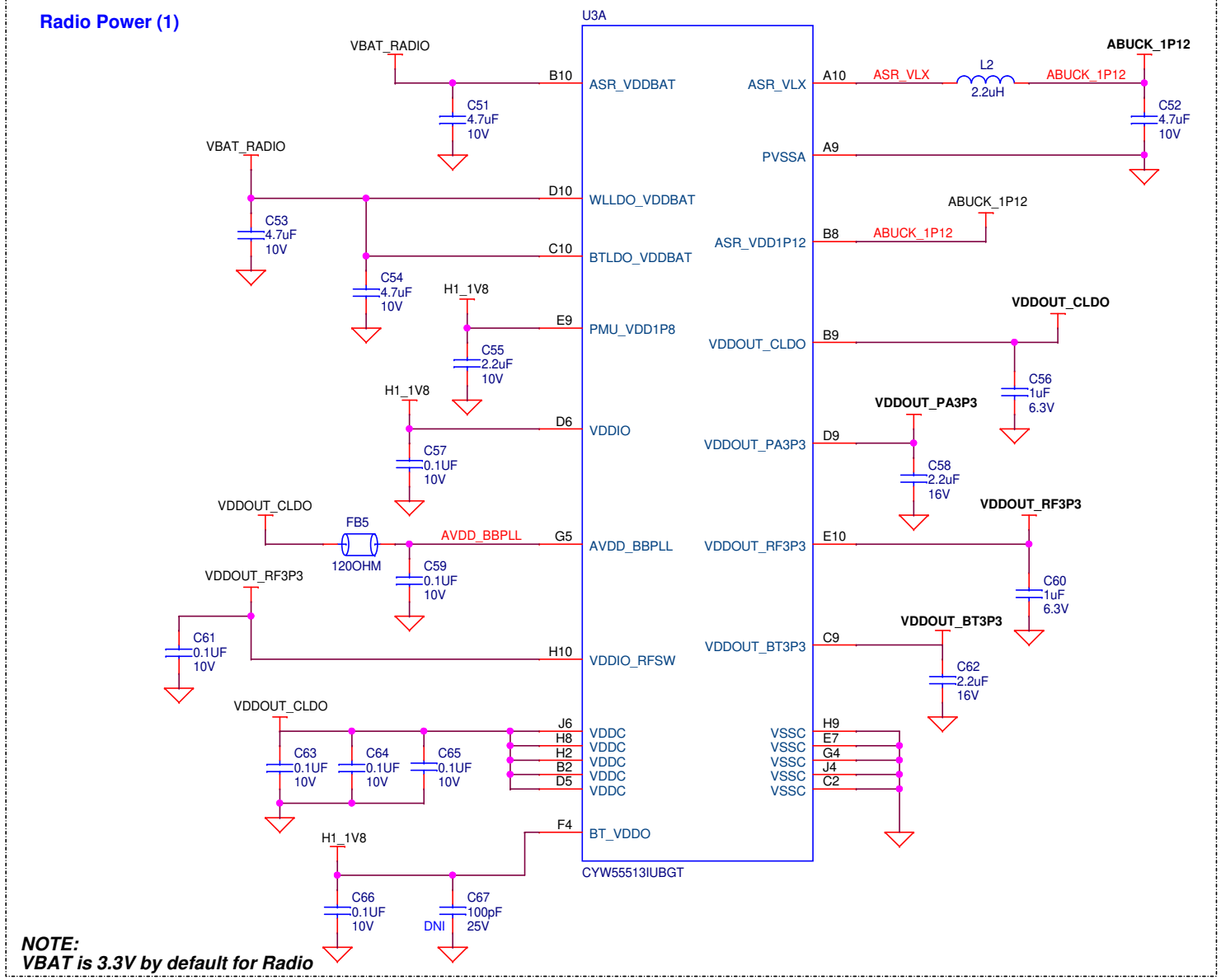




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SCH Title : PSOC™ Edge E84 SoM				
Page Title: Tripad and RC for OA				
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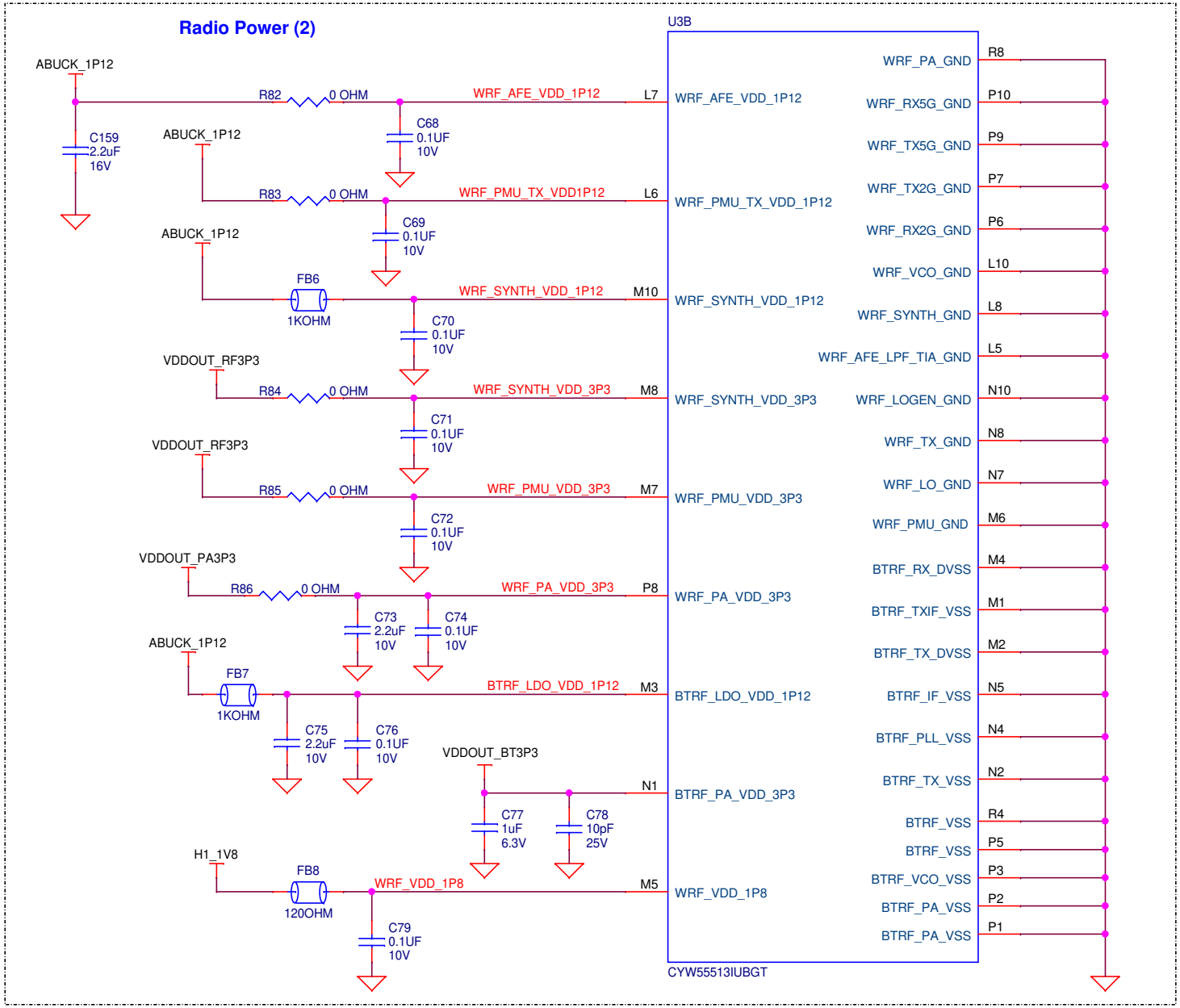
Radio Power (1)



NOTE:
VBAT is 3.3V by default for Radio

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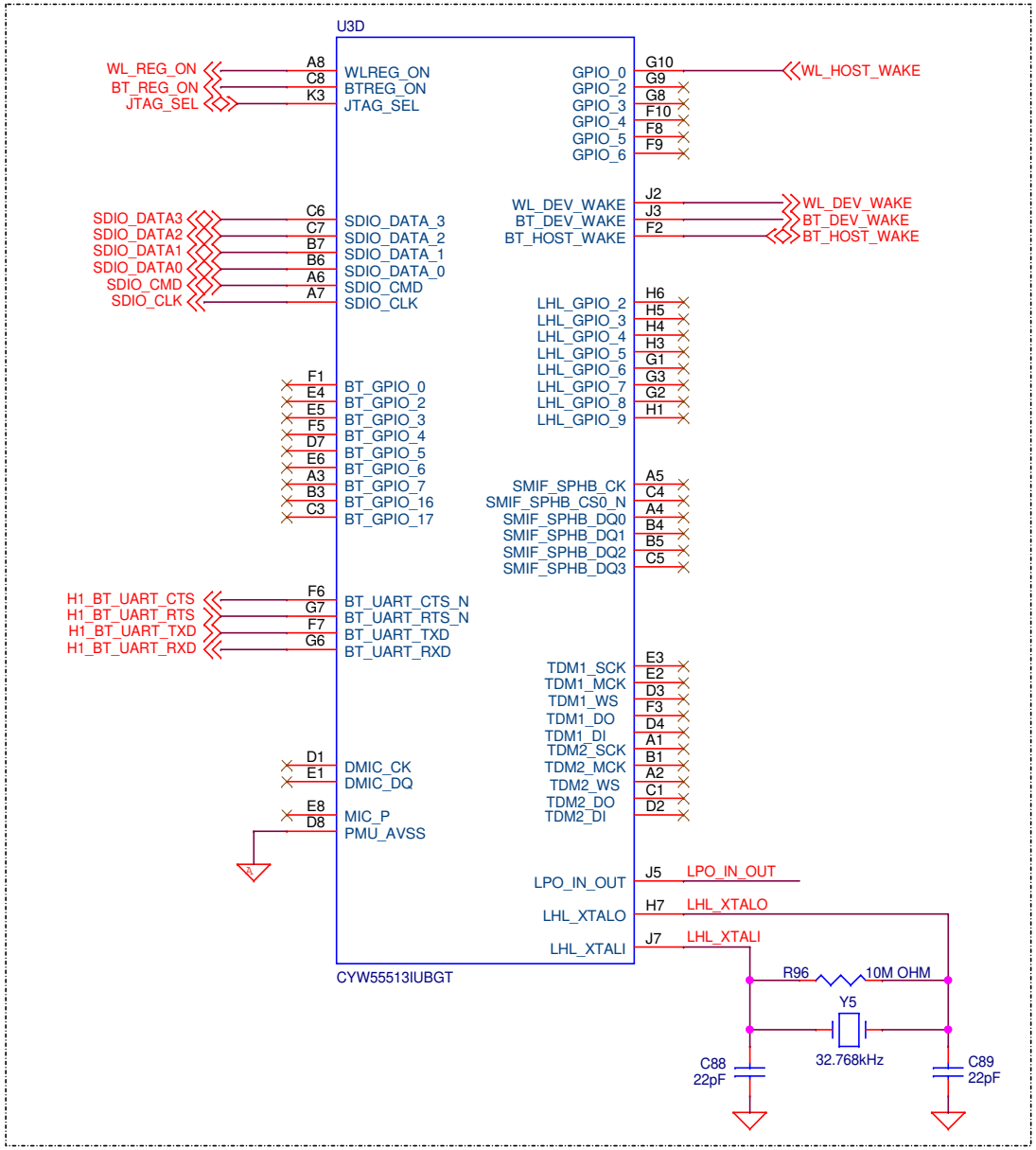
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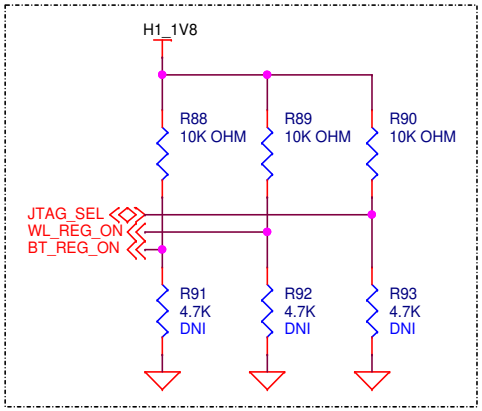


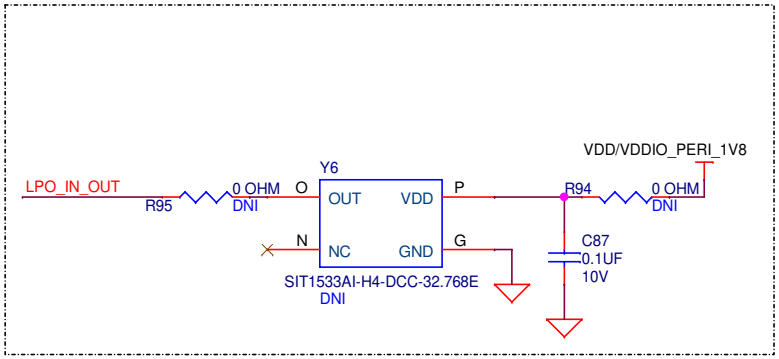
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SCH Title : PSOC™ Edge E84 SoM				
Page Title: Radio PMU Topology_2				
Size	Document Number	Drawn By	Approved By	Rev
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Date:	Wednesday, November 04, 2026	Sheet	**12** of **20**	

Radio Subsystem

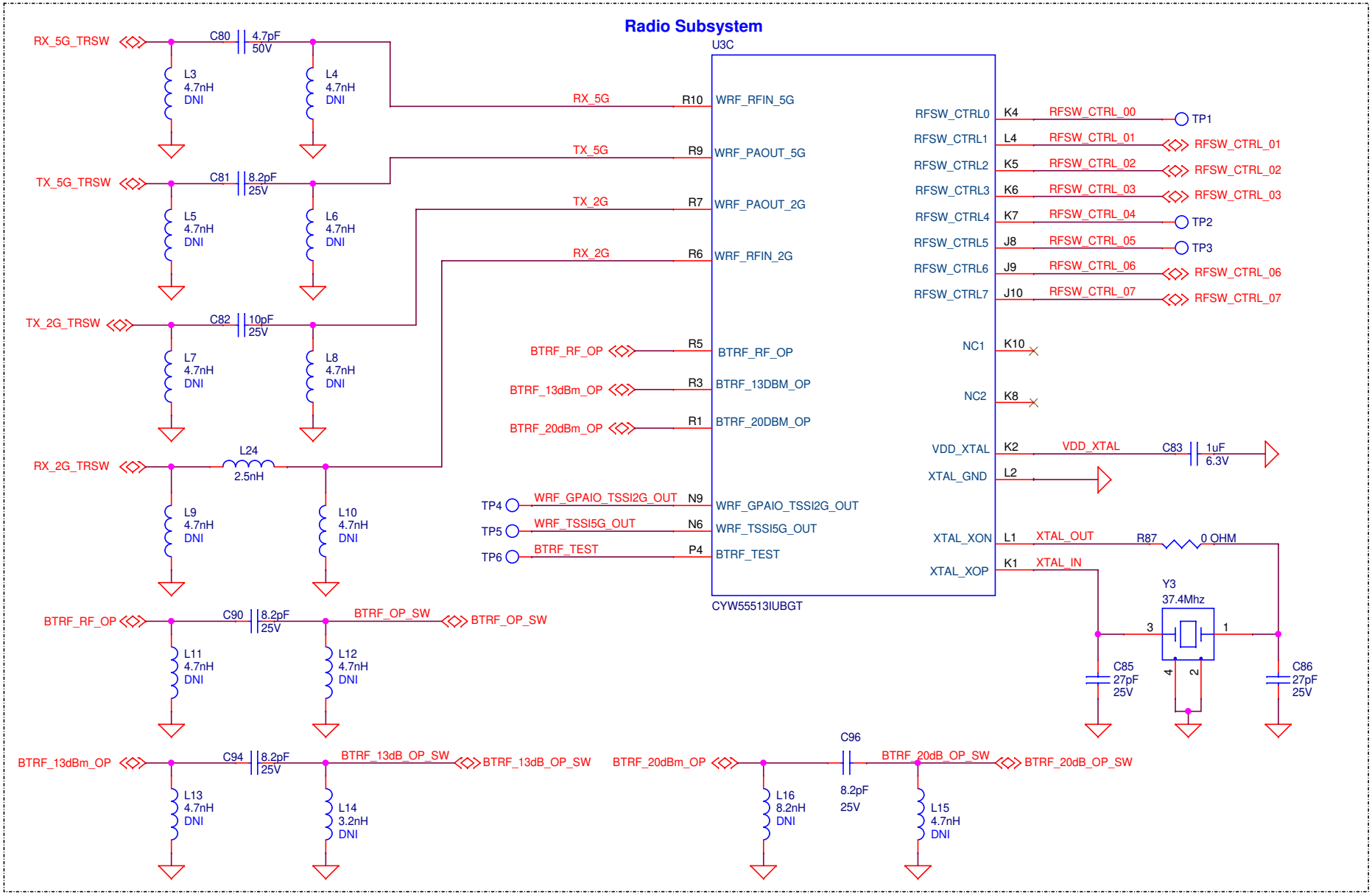






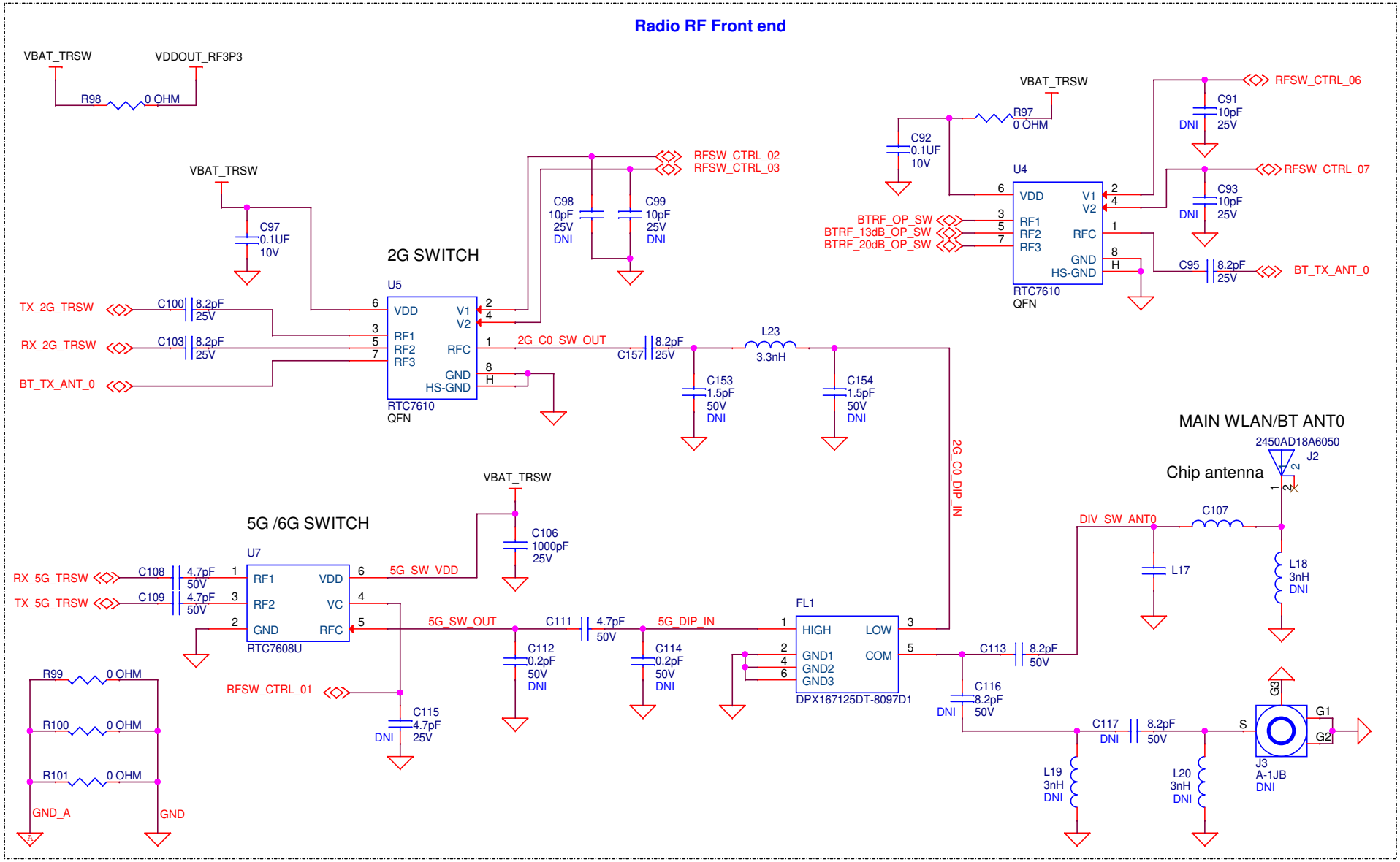
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SCH Title : PSOC™ Edge E84 SoM				
Page Title: Radio System Section				
Size	Document Number	Drawn By	Approved By	Rev
A4	**630-60673-01**	**RAJB**	**RKPM**	**4.1**
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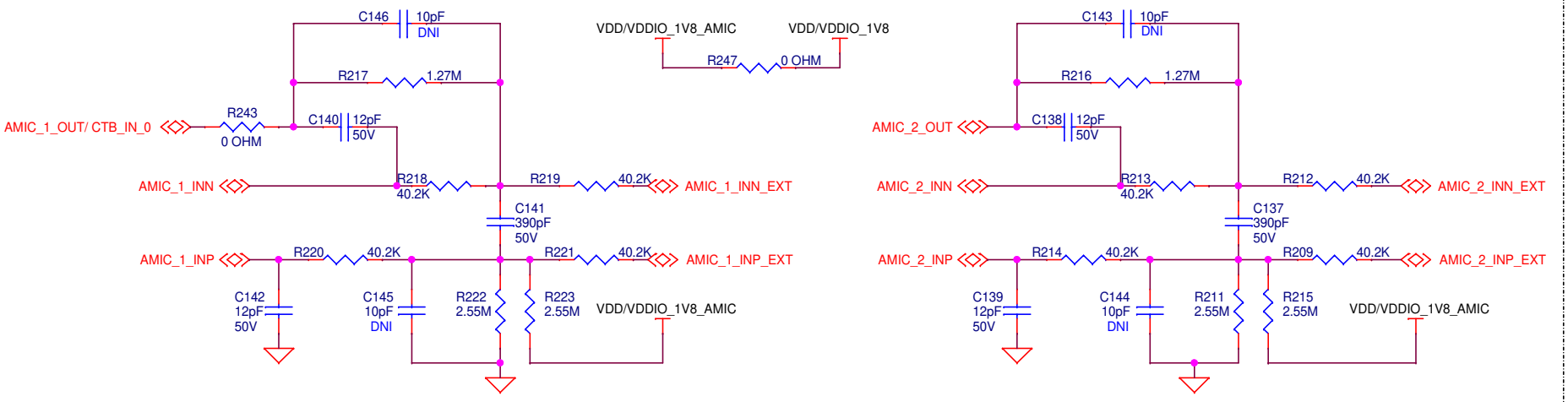
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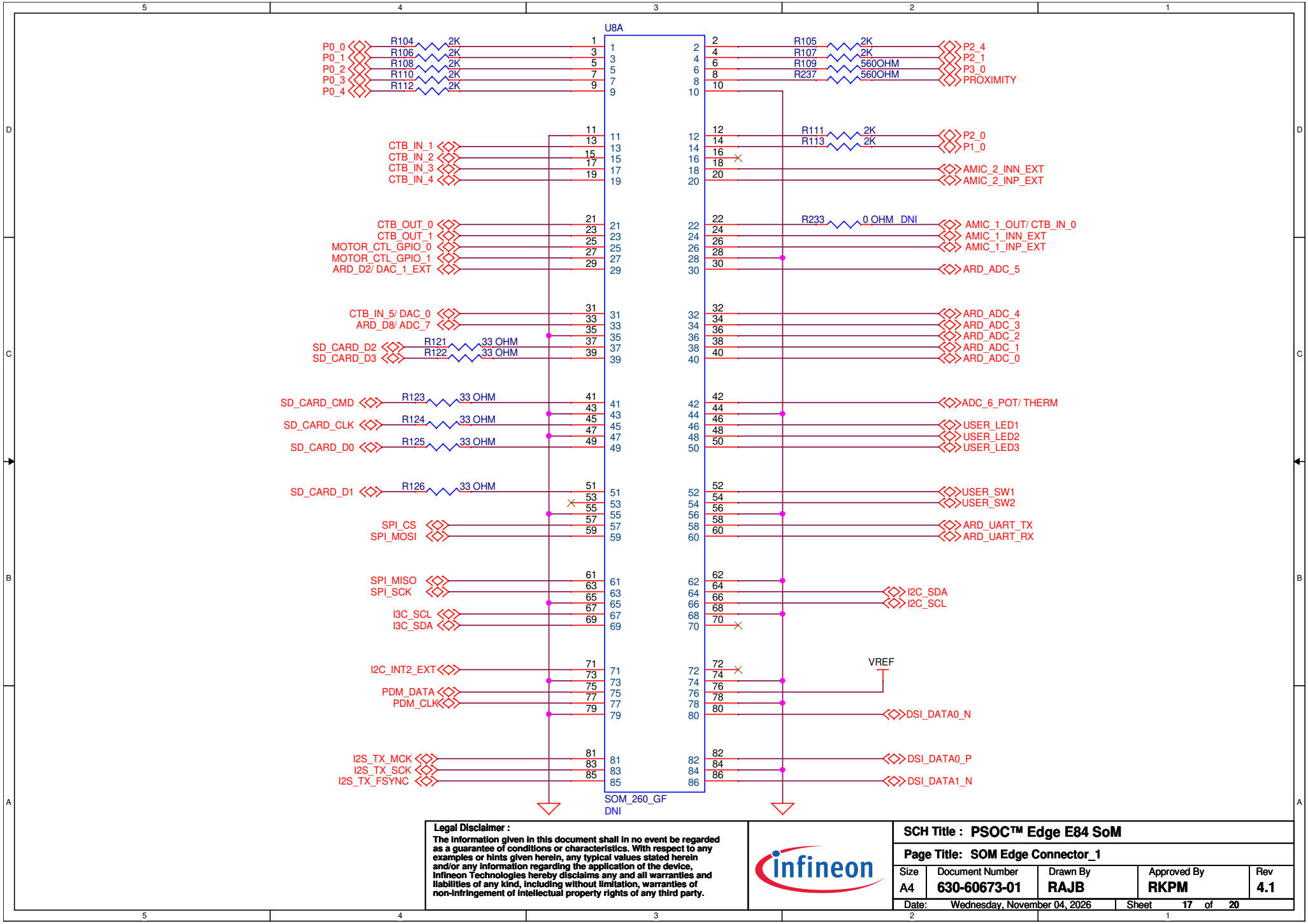
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Page Title: Radio RF Front end				
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A4	630-60673-01	RAJB	RKPM	4.1
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AMIC Signal Processing



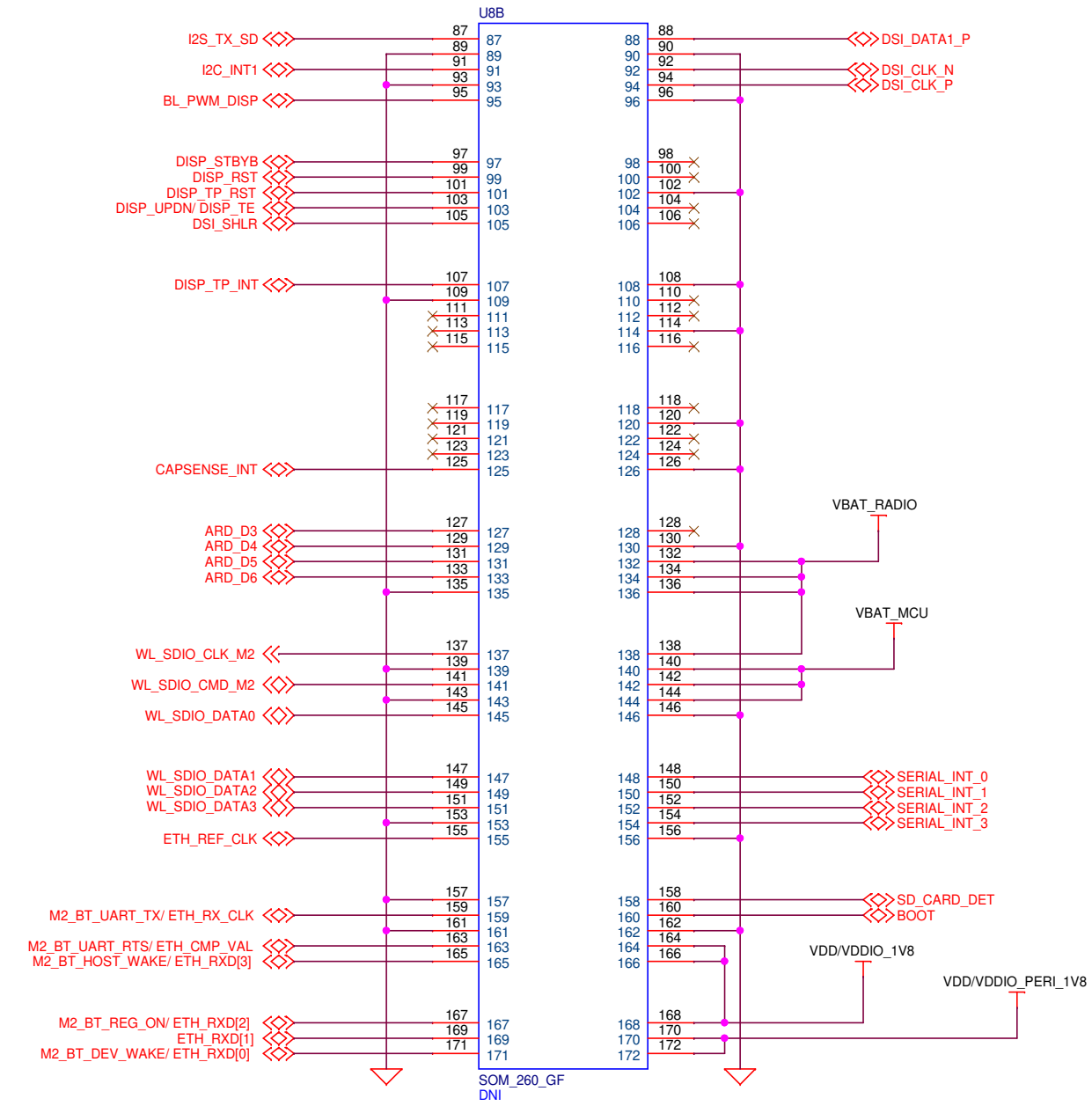
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Page Title: AMIC Signal Processing				
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A4	630-60673-01	RAJB	RKPM	4.1
Date: Wednesday, November 04, 2026		Sheet 16 of 20		



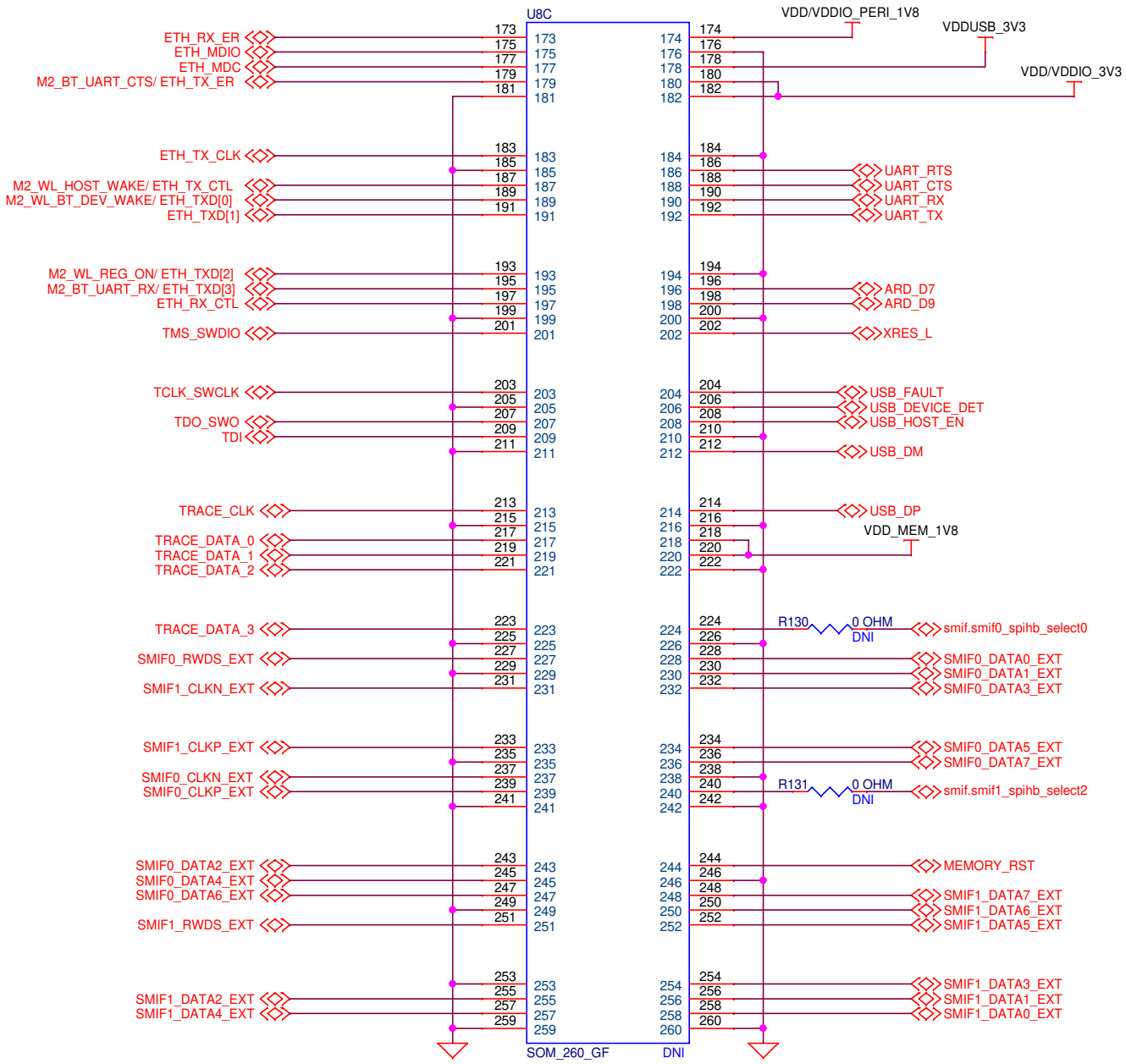
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SCH Title : PSOC™ Edge E84 SoM				
Page Title: SOM Edge Connector_1				
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Size	Document Number	Drawn By	Approved By	Rev
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Page Title: SOM Edge Connector_3				
Size	Document Number	Drawn By	Approved By	Rev
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Date: Wednesday, November 04, 2026		Sheet 19 of 20		

REVISION HISTORY

REV	DESCRIPTION OF CHANGE	ORIG. OF CHANGE	DATE
1.0	Initial release	RAJB	08/08/2023
2.0	1. P7.2 is connected to CapSense co-processor reset (has default pull up) 2. EXT clk is connected to P7.4 3. XRES pullup added 4. VDDD and VCCD shorted with common supply on PSOC4000T section	RAJB	31/10/2023
3.0	1. R209, R212-R214, R218-R221, C137, C141 component values changed in AMIC signal processing ckt. section. 2. MCU configured for VBAT domain 3. TP12 added for VCCD 4. R244, R245 added to segregate the memory powers 5. R246 pullup added on Memory reset IO 6. R242 powered from VDDD (instead of VDD/VDDIO_1V8) 7. CapSense section power is taken from VDD/VDDIO_PERI_1V8 (instead of H1_1V8) 8. Made C124 populated 9. Made D5 as DNI 10. R186 removed to bypass U13 and added in series with H1_1V8 11. R247 added to disconnect AMIC section voltage dividers. 12. R243 multiplexing resistor added 13. RF frontend L17 and C107 parts are swapped	RISR	03/04/2024
4.0	1. Cosmetic changes and clean-up of passive component properties	RKPM	05/08/2025
4.1	1. AMIC front end, passives updated	RKPM	04/11/2026

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