

Dr. Susanne Lachenmann

Chairwoman of the general works council,
Infineon Technologies AG

Member of the Infineon Technologies
Supervisory Board since 2015
(elected until 2025)



Personal information

Year of birth	1967
Residence	Ravensburg
Nationality	German

Education

1995	PhD in Natural Science (Dr. rer. nat.), Eberhard-Karls-University Tübingen
1993	Diploma in physics, Eberhard-Karls-University Tübingen

Professional career

Since 2024	Chairwoman of the general works council, Infineon Technologies AG
Since 2020	Principal Engineer, Infineon Technologies AG
Since 2008	Lead Development Engineer for Design Enabling & Services, Infineon Technologies AG Design methodology, physical verification (Principal Parasitic Extraction Methodology)
2005 – 2008	Development Engineer, Qimonda AG Design for KERF and test chips
1999 – 2005	Development Engineer for memory products, Infineon Technologies AG Various design divisions: KERF and test chips, embedded DRAM; Design methodology, technology package development
1998 – 1999	Development Engineer for memory products, Siemens AG Design methodology, technology package development
1995 – 1998	Research assistant at the Institute of Thin Film and Ion Technology, Research Center Jülich

Memberships in other statutory supervisory boards in Germany

None

Memberships in comparable supervisory bodies of business enterprises in Germany and abroad

None

